

DVB-T MODULATOR

DVB-T Modulator is made according to DVB-T standard output the RF signals, which can be transmitted through HFC and MMDS networks. It is widely used in digital TV, data broadcasting, VOD, Internet, video conference systems. The WDQ-3300 has very convenient management interface, the user can complete all operation via local keyboard and LCD display on device, or access through the Ethernet port on device.



Features

- Compliant to EN300744 standard
- Support 6MHz/7MHz/8MHz band width
- Error correction follows DVB-T standard
- Operable in QPSK, 16QAM, 64QAM mode
- Support 2K/8K carrier-wave operating mode
- Input interface: ASI and SPI(DS3/E3 optional)
- Input bit rate:1.5~31.67Mbps
- Output band width: 6/7/8MHz
- 188 and 204 packet automatic identification, shows real-time data format
- Advance output TS efficiency
- Capable of NIT mapping and sending; supports over 44 NIT mapping frequency
- Holds 8 groups of PID filtering and re-mapping function
- Support all PSI/SI data abstraction, modification and analysis
- Null packet filtering and filling
- Performs multiple PCR correction simultaneously
- Monitor and test real-time enter/valid bit rate
- Self-adjust filter circuit design in order guarantees superb SNR (out of band)
- -20dB RF test output is available (IF output is options)
- Failure alarming and power off memory
- Local keyboard control and LCD display, or access via Ethernet link

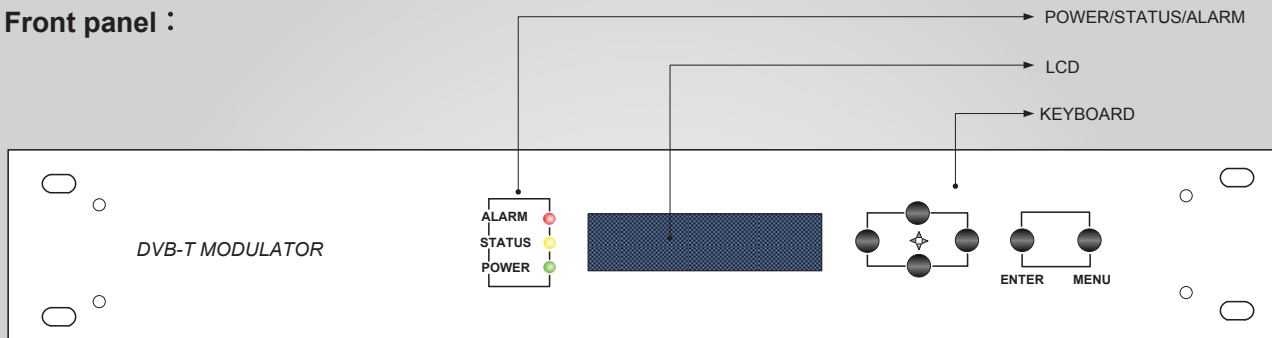


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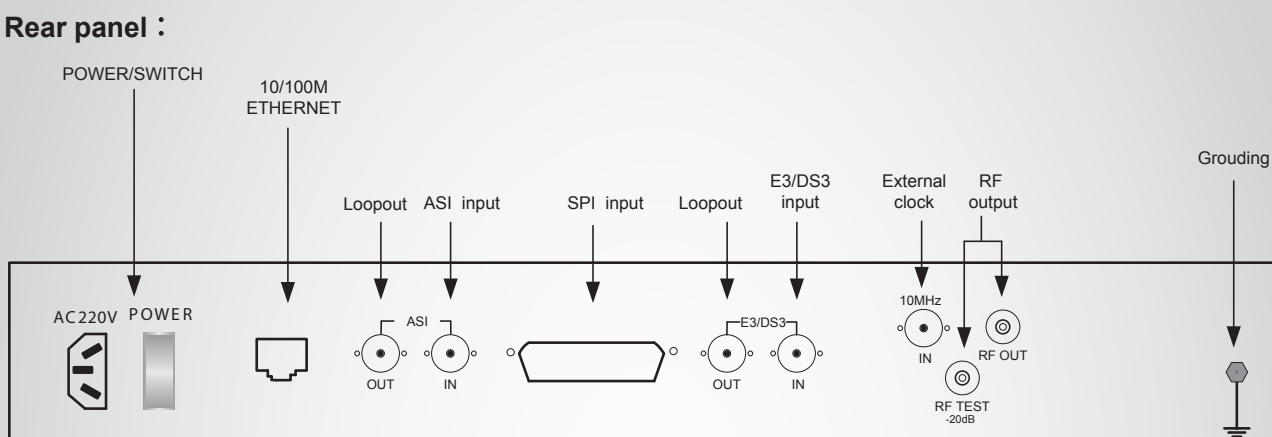
Technical Specifications

Parameter	Symb ol	Description	Unit	Remark
Mechanical dimension				
Height	H	42	mm	About 1U
Width	W	434	mm	About 17"
Depth	D	340	mm	
ASI Input (with loopout)				
Number of ASI input	-	1		
Impedance	-	75		
Connector	-	BNC	NA	
Maximum bit rate	-	108	Mbps	
TS packet format	-	188 or 204	Byte	Automatic detection
Transmission format	-	Data packet or data burst	NA	
SPI Input				
Number of SPI input	-	1	NA	
Connector	-	DB-25 female	NA	
Packet format	-	188 or 204	Byte	without channel coding
Maximum bit rate	-	108	Mbps	LVDS mode
E3/DS3 Input(options, with loopout)				
Number of E3/DS3 input	-	1	NA	G.703 standard
Impedance	-	75		
Connector	-	BNC	NA	
TS packet format	-	188 or 204	Byte	
Data bit rate	-	E3/DS3 (34.368/44.736)	Mbps	
10MHz Clock Reference				
Internal Stability	-	0.1	ppm	
External Impedance	-	50		
External Connector	-	BNC	NA	
External Input level		55~70	dBuV	
RF Output(with -20dB test output)				
Impedance	-	75		
Connector	-	BNC	NA	
Frequency	-	48~860	MHz	
Output level	-	100~115.5	dBuV	Step: 0.5 dBuV
Modulation Specification				
Modulation scheme	-	QPSK,16,64QAM		
FEC	-	1/2、2/3、3/4、5/6、7/8		
Guard Interval	-	1/4、1/8、1/16、1/32		
RS encode	-	204byte mode		
IFFT	-	2K/8K		
Modulation Error Rate	MER	≥40	dB	
Signal to noise ratio	SNR	≥50	dB	
Bit Error Rate	BER	≥9x10E -9		

Front panel :



Rear panel :



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